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(54) Imaging systems

(57) A sequential color display system (10) suited to receive data (20) at different rates without discarding or filtering video data. The mismatch of bandwidths between the input source video data and the output sequential color display is accommodated by retaining a modified synchronous operation. A color wheel (50) having a plurality of segments is utilized whereby the input frame rate in reference to the color wheel rate is

always a ratio of integers. Using a digital micromirror device (44) spatial light modulator, no mixing of frame-to-frame bit planes is required during a colored segment. In the preferred embodiment, the ratio of integers is 7-to-6 for a 50 hertz input, such as a PAL system, or a 5-to-6 ratio for a 72 hertz input. The color wheel is maintained at about a 60 hertz rate.

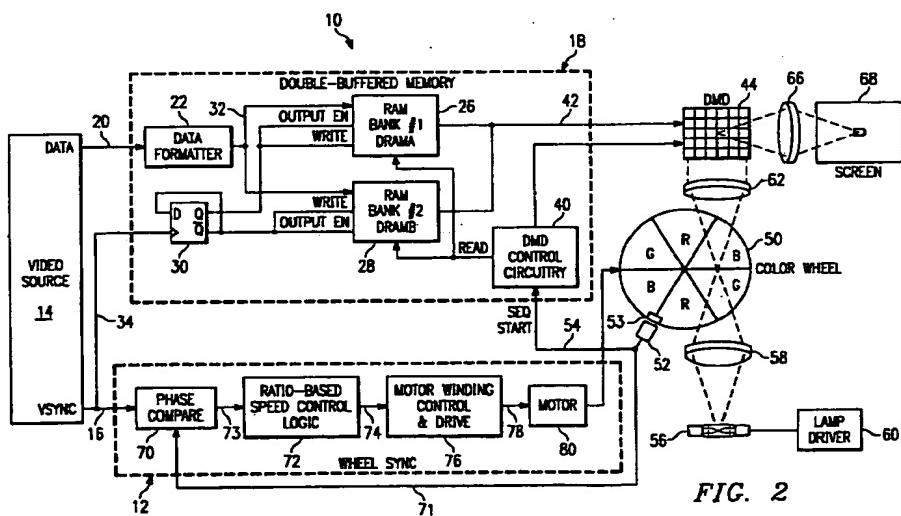


FIG. 2

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Description**FIELD OF THE INVENTION**

This invention relates generally to imaging systems such as those including a spatial light modulator, and more particularly to a method and apparatus for performing sequential color imaging from input data having a varying data frame rate.

BACKGROUND OF THE INVENTION

Spatial Light Modulators (SLMs) are widely used in the industry for video monitors, graphic displays, projectors, and hard copy printers. SLMs are devices that modulate incident light in a spatial pattern corresponding to an electrical or optical input. The incident light may be modulated in its phase, intensity, polarization, or direction. This light image is directed and focused to a screen in the case of a projector, video monitor or display, or is ultimately focused on a light sensitive material, such as a photoreceptor drum, in the case of a xerographic printer.

For convenience the term light will hereinafter be used to refer to radiant energy at visible, infra-red, ultra-violet and x-ray wavelengths.

The light modulation may be achieved by a variety of materials exhibiting various electro-optic or magneto-optic effects, and by materials that modulate light by surface deformation. Other spatial light modulators may include tiny micro-mechanical devices comprising an array of positionable picture elements (pixels). The light image can be colored if it is to be displayed on a screen of a projector, monitor, or a television and the like. This coloring is typically done in one of two ways, either using non-sequential color systems, or using sequential color systems. A non-sequential color system simultaneously generates multiple colors of light, such as red, green and blue light. An example of a non-sequential color system is discussed in commonly assigned US Patent Number 5,452,024, entitled "DMD Display System". In sequential color systems, color images are generated by sequentially projecting colored light, i.e. red, green and blue light, in a single image frame, which typically lasts 1/60 of a second for a 60 Hertz system. Sequential color systems typically utilize a color wheel that is partitioned into a plurality of colored segments, such as a red, green, and blue segment, or multiples/combinations thereof. An example of a sequential color system is disclosed in commonly assigned US Patent Number 5,448,314, entitled "Method and Apparatus for Sequential color Imaging".

A recent innovation of Texas Instruments Inc. of Dallas, Texas is an imaging system using an SLM having an array of individual micro-mechanical elements, known as a digital micromirror device (DMD), also referred to as a deformable mirror device. The DMD is a spatial light modulator suitable for use in displays, projectors and hard copy printers. The DMD is a monolithic

single-chip integrated circuit, comprised of a high density array of 16 micron square deflectable micromirrors on 17 micron centers. These mirrors are fabricated over address circuitry including an array of SRAM cells and address electrodes. Each mirror forms one pixel of the DMD array and is bi-stable, that is to say, stable in one of two positions. A source of light directed upon the mirror array will be reflected in one of two directions by each mirror. In one stable "ON" mirror position, incident light to that mirror will be reflected to a projector lens and focused on a display screen or a photosensitive element of a printer, and forms an image of the mirror/pixel. In the other "OFF" mirror position, light directed on the mirror will be deflected to a light absorber. Each mirror of the array is individually controlled to either direct incident light into the projector lens, or to the light absorber. In the case of a display, a projector lens and a light prism ultimately focus and magnify the modulated light image from the pixel mirrors onto a display screen and produce a viewable image. If each pixel mirror of the DMD array is in the "ON" position, the displayed image will be an array of bright pixels.

For a more detailed discussion of the DMD device, cross reference is made to US Patent 5,061,049 to Hornbeck, entitled "Spatial Light Modulator and Method"; US Patent 5,079,544 to DeMond, et al, entitled "Standard Independent Digitized Video System"; and US Patent 5,105,369 to Nelson, entitled "Printing System Exposure Module Alignment Method and Apparatus of Manufacture", each patent being assigned to the same assignee of the present invention. Gray scale of the pixels forming the image can be achieved by pulse width modulation techniques of the mirrors, such as that described in US Patent 5,278,652, entitled "DMD Architecture and Timing for Use in a Pulse-Width Modulated Display System", assigned to the same assignee of the present invention.

In non-sequential color systems, three (3) DMD arrays can be used to modulate colored light, one each for red, green, and blue light, as disclosed in the commonly assigned Patent Number 5,452,024, titled "DMD Display System". In contrast, a sequential color system requires only one such DMD device, with the red, green, and blue light being sequentially modulated and reflected by the single DMD array to a screen. The need for three such arrays in the non-sequential color system triples the requirement for the DMD arrays, and attendant hardware over the sequential color system, but offers increased display brightness. Thus, there is a trade off between the complexity, cost and performance of a non-sequential color system when viewed against a sequential color system.

In the case of a sequential color system, a single light source is typically used, such as disclosed in US Patent No. 5,101,236 to Nelson, et al, entitled "Light Energy Control System and Method of Operation", assigned to the same assignee as the present invention. These lamps may typically be comprised of a xenon or metal halide arc lamp, or lasers. This arc lamp

may be powered by an AC or DC power source.

In conventional display systems, the video frame rate or refresh rate of the display is typically 60 hertz (Hz), or 60 frames a second. However, the incoming (source) data frame rate may vary from 60 frames a second. For instance, while a typical NTSC signal provides video frame data at 60 hertz, the video source frame rate for a PAL system is 50 hertz. Video or computer graphics data, meanwhile, is received at many other frame rates other than 60 hertz, typically being 72 hertz.

Typically, when the input frame rate deviates from 60 hertz in a 60 hertz sequential color display system, data must be discarded or filtered (e.g. averaged) before video is displayed due to the mismatch of bandwidths between the input source video and the output color sequential display rate. Rather than display video at 72 hertz for a 72 hertz input, the incoming data is discarded or filtered to maintain a 60 hertz video display frame rate. Discarding data will generate motion artifacts. Filtering data adds components and system cost. If incoming video data is received at a slow rate, operating in synchronism at a slower display rate, like 50 hertz, would generate a noticeable flicker in the display to the observer provided the screen is bright enough. Therefore, the data is averaged to maintain a 60 hertz display rate. Thus, retaining synchronous operation may not be sufficient for high quality video.

Additional problems arise in a DMD display system since data is displayed in a bit-plane format. One solution to the input/output frame rate mismatch, when doing color sequential displaying of data, is to asynchronously read the data from frame buffers. However, since the data displayed during any color segment may be from two different frames, a mixing of bit-plane information from these two frames occurs. This mixing of bit-plane can result in significant degradation of signal-to-noise ratio especially near MSB transition codes when using pulse width modulation techniques to determine the "ON" time of the mirrors during one video frame. To illustrate this, referring to Figure 1, consider a DMD with 8-bits of color code, without bit-splitting, where the MSB is displayed during the entire first half of each color segment. If a pixel code 80hex (midscale) from the source has a slight amount of noise, then 7Fhex may be displayed during the first half of a color segment and 80hex during the second half if two different frames are displayed during this color segment. A code of 00hex would be displayed resulting in a half-scale error on the display as shown in Figure 1. This is an extreme example, but even with bit-splitting during the color segments, the noise generation is significant when using an asynchronous frame buffer approach for frame rate conversion.

It is desirable to maintain the output color sequencing display frame rate of 60 hertz, even when the input source video frame rate is not at 60 hertz, without discarding, filtering or averaging video data. In addition, for DMD applications, it is desirable to eliminate the signal-

to-noise degradation due to frame-to-frame bit-plane mixing during color segments without resorting to forcing the color sequencing rate to match the source video frame rate.

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SUMMARY OF THE INVENTION

The present invention achieves technical advantages as a display system by using a color wheel having a plurality of colored segments, whereby the source frame transition time always occurs at a colored segment transition, but could also occur at the middle of a colored segment. In other words, the color wheel rate and the source video frame rate is always a ratio of integers. In this way, over time, the source frame transition time rotates around the colored segments facilitating no discarding of data, no filtering for processing, and no mixing of frame-to-frame bit-planes for DMD displays. The ratios are maintained accurate enough so that the color sequence rate can be maintained at about 60 hertz while the source video rate is different than 60 hertz. For instance, if the input source video rate is 50 hertz, which is typical of a PAL format signal, the color wheel is rotated at about 58.33 hertz, which is an integer ratio of 5-to-6. If the incoming source video rate is 72 hertz, which is typical of video or computer graphics data, the color wheel is rotated at 60 hertz. This corresponds to a ratio of integers being 7-to-6. In this way, the source frame transition time of incoming data always occurs at a colored segment transition or at the middle of a colored segment of the color wheel. Preferably, the color wheel has six segments, comprising of two red, two green, and two blue segments, preferably which alternate around the wheel, however, at least two segments of like color could be adjacent one another.

According to the preferred embodiment of the present invention, the display system comprises a spatial light modulator, a light source providing light, and a color wheel rotating at a wheel rate and coloring the light, this colored light illuminating the spatial light modulator. A logic circuit has an input receiving frames of data at a frame rate and communicating this data to the spatial light modulator. A control circuit detects the frame rate, and adjusts the color wheel rate to maintain a ratio of integers with reference to the frame rate. The color wheel has multiple colored segments, where each segment is illuminated equally in time by the light source for each of the frames of data, except for one or more segments, when the frame rate is lower or greater than the wheel rate. However, the transition time between the incoming frames of data to be displayed always occurs at the transition between a pair of the colored segments being illuminated by the light source, or in the middle of a colored segment to avoid generating artifacts.

Preferably, the color wheel has a plurality of pairs of colored segments, wherein each segment, except at least one, is illuminated for each frame of incoming data when the incoming frame rate is lower than the prede-

terminated wheel rate. When the incoming frame rate is higher than the predetermined wheel rate, at least one segment is illuminated one more time than the other colored segments while displaying one frame of incoming data. In other words, for a color wheel having N colored segments, N-1 segments, N-2 segments etc. are illuminated each data frame when the incoming frame rate is lower than the predetermined wheel rate, but where N+1 segments, N+2 segments etc. are illuminated each data frame when the incoming frame rate is higher than the predetermined wheel rate. In the preferred embodiment, the ratio of incoming frame rate to the wheel rate is 7-to-6 when the incoming frame rate is lower than the wheel rate. However, the ratio of the incoming frame rate to the wheel rate is 5-to-6 when the incoming frame rate is higher than the wheel rate. If the incoming frame rate is the predetermined wheel rate; i.e. 60 hertz, the ratio is 1-to-1 and the rates match.

The control circuit provides a wheel motor sync output at a rate being a function of a vertical sync rate of the frames of incoming data. The sync output rate is unequal to the vertical sync rate when the vertical sync rate deviates from a predetermined rate, whereby the wheel rate is a function of the sync output. However, the sync output rate is identical to the vertical sync rate when incoming frame rate is identical to the predetermined rate.

The method of operating the sequential display system comprises utilizing a color wheel having a plurality of colored segments to color source light illuminating a spatial light modulator, the color wheel rotating at a wheel rate. The colored light is modulated with the spatial light modulator according to the frames of data received at a frame rate. The wheel rate is controlled to be a ratio of integers with respect to the frame rate, wherein the ratio of integers is other than 1-to-1 except when the incoming frame rate is identical to a predetermined desired wheel rate. Preferably, a color wheel is implemented having at least six colored segments of at least three different colors. The transition time between the frames of source data always occurs when a transition between the wheel colored segments is proximate the source light, or when the middle of a colored segment is proximate the source.

By insuring the color wheel rate in reference to the incoming data frame rate is a ratio of integers, the incoming frames of data can be displayed whereby the transition time between the frames of data occurs at the interface of the wheel colored segments. Alternatively, this transition time can take place at the middle of a colored segment. The interface between the wheel colored segments is commonly known as a spoke. The present invention is a spoke-synchronous frame rate display system that maintains a spoke being in synchronism with the incoming data frame rate. When the incoming frame rate and the wheel rate are not matched, the transition point for incoming frames of data sequentially occurs at a different colored segment boundary. For a six color segment, having six spokes,

each spoke is synchronized to sequentially match with the transition point for input frames of data every six rotations.

5 BRIEF DESCRIPTION OF THE DRAWINGS

The present invention will now be further described, by way of example with reference to the accompanying drawings in which;

10 Figure 1 is an illustration of bit-plane information mixing when asynchronously reading data from frame buffers;

15 Figure 2 is a block diagram of a spoke-synchronous colored sequential system according to the preferred embodiment of the present invention, whereby the incoming data frame rate as compared to the color wheel rate is always a ratio of integers;

20 Figure 25 is an illustration of the spoke-synchronous frame rate conversion according to the invention when the incoming frame rate, i.e. 50 hertz, is less than the color wheel rate, whereby the color wheel rate is maintained to be a ratio of integers with respect to the input frame rate, i.e., a 7-to-6 ratio; and

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Figure 3 is an illustration of the spoke-synchronous frame rate conversion according to the invention when the incoming frame rate, i.e. 50 hertz, is greater than the color wheel rate, whereby the color wheel rate is maintained to be a ratio of integers with respect to the input frame rate, i.e., a 5-to-6 ratio;

30 Figure 4 is an illustration of the spoke-synchronous frame rate conversion according to the invention when the incoming frame rate is greater than the color wheel rate, again, whereby the color wheel rate is maintained to be a ratio of integers with respect to the incoming frame rate, i.e., a 5-to-6 ratio.

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DESCRIPTION OF THE PREFERRED EMBODIMENT

Referring now to Figure 2, there is generally shown 40 at 10 a sequential color imaging display system according to the preferred embodiment of the present invention. System 10 is seen to include a control circuit generally shown at 12 being responsive to the vertical sync VSYNC signal of a video source 14 provided on line 16, and a logic circuit generally shown at 18 being connected to and responsive to frames of incoming video data provided by source 14 on line 20.

45 Incoming frames of video data provided by source 14 on line 20 are formatted by a data formatter 22. If this data is not already in the digital domain, it is first converted by an analog-to-digital circuit, not shown, and then formatted. For ease of illustration of the present invention, the incoming video data provided on line 20 is to be understood as already being in the digital domain. Data formatter 22 formats this stream of digital data, and alternately writes frames of this data into a pair of RAM memory banks 26 and 28, identified as DRAM A and DRAM B. To alternately write these frames of data into memory banks 26 and 28, a D flip-flop circuit 30 is

utilized to alternately enable and permit writing of the data from formatter 22 on line 32 into the respectively enabled memory bank 26 or 28. That is to say, for each VSYNC pulse provided on input line 34 to the clock input of circuit 30, the output enable line OUTPUT EN and the write control line WRITE of alternating memory banks will be enabled.

A DMD control circuit 40 alternately reads the data from these two memory banks 26 and 28 for successive frames of data to be displayed. The data is output on line 42 and written into the memory cells of a DMD spatial light modulator shown at 44. DMD control circuit 40 is responsively synchronized to the color segment being illuminated of a color wheel generally shown at 50. This is done utilizing an optical sensor 52 to sense a reference point 53 of wheel 50 each time it is rotated therepast. Sensor 52 provides a reference signal or tick on line 54 to DMD control circuit 40 to initiate the sequence start by circuit 40 for the writing of data from one memory bank 26 or 28 to DMD data 44. By sensing the reference signal on line 54, DMD control circuit 40 is synchronized to read data from one of the memory bank 26 or 28 corresponding to the colored segment currently being illuminated by a light source 56, with light focused thereupon by a lens 58. A lamp driver circuit 60 controls the illumination of light source 56. In this way, DMD control circuit 40 will read data associated with the red, green or blue data which corresponds to which colored segment of wheel 50 is being illuminated, and loads the color data into the DMD 44. In this way, the pixel mirrors of SLM 44 modulate the colored light incident to the array depending on whether the source light is red, green, or blue. A lens 62 is utilized to homogeneously illuminate DMD 44, whereby a projection lens 66 is utilized to focus the light image formed by SLM 44 on a screen 68.

The data write algorithm and apparatus described so far is discussed in more detail in the commonly assigned Patents and Patent applications detailed in the section Background of the Invention, and is well understood in the art. Depending on the amount of data in each frame received by formatter 22, two or more banks of memory can be utilized and controlled by DMD control circuitry 40. At least two banks of memory are utilized so that one bank can be read while the other bank is being written into. That is to say, the contents of one memory bank are being selectively unloaded on line 42 and provided to DMD 44 to modulate the incident colored light illuminating the DMD mirror array 44, while the next frame of data to be loaded into DMD 44 is being written into the other memory bank.

According to the preferred embodiment of the present invention, control circuit 12 will now be discussed in considerable detail. Basically, control circuitry 12 detects and calculates the period between consecutive vertical sync VSYNC pulses to determine the frame rate of the video data provided by source 14. If the data frame rate is determined to be other than a predetermined 60 hertz, the circuitry 12 establishes the color

wheel 50 to have a rotation rate to be an integer ratio with respect to the frame rate of the incoming data. Circuitry 12 insures that the transition time between frames of incoming data provided on input line 20 correspond in time to when one of the six spokes, the spokes being the interface between adjacent colored segments of wheel 50, is being illuminated by light source 56. Alternatively, circuitry 12 can insure a frame transition occurs at the center of a colored segment. The segment may be illuminated at the frame transition.

If the incoming data frame rate is 50 hertz, i.e. a PAL signal, the color wheel 50 is rotated at 58.33 hertz, which is a 5-to-6 ratio. If the incoming data rate is 72 hertz, i.e. corresponding to computer graphics data, the color wheel 50 is rotated at 60 hertz, which is a 7-to-6 ratio. Of course, for other frame rates of incoming signals, different ratios can be established while maintaining approximately a 60 hertz display rate. With the color wheel 50 being partitioned into six colored segments, having two segments of red, two segments of green and two segments of blue, the transition time between frames of data provided on input line 20 corresponds in time to when one of the six spokes of wheel 50 are being illuminated by light source 56 and lens 58. This can be further understood by example and will be now discussed.

Still referring to Figure 2, control circuit 12 is seen to comprise a phase comparator circuit 70. Comparator 70 senses the time period of consecutively sensed vertical sync VSYNC pulses provided on input line 16 in reference to the time period between the wheel sync pulses provided by sensor 52 on line 71, and provides an error signal on line 73. A ratio-based speed control logic circuit 72 is responsive to the error signal output by this phase comparator circuit 70 on line 73. The speed control logic circuit 72 provides an output on line 74 to a motor winding control and drive circuit 76, which in turn provides a control signal on line 78 to wheel motor 80. If, for example, the phase comparator circuit 70 senses the period between vertical sync VSYNC pulses to have a duration of 16.66 microseconds, speed control logic circuit 72 directs the motor winding control and drive circuit 76 to rotate motor 80 at 60 hertz, maintaining a zero error on line 73. In this situation, the color wheel 50 is rotated in synchronism with the incoming data rate, 60 hertz.

If phase comparator circuit 70 senses consecutive vertical sync VSYNC pulses to have a 20ms period, corresponding to 50 hertz which is typical of a PAL system, while the wheel sync pulses have a period of about 16.66ms, a negative error is output by comparator 70 on line 73. The ratio based speed control logic circuit 72 responds to the negative error by instructing motor winding control and drive circuit 76 to slow the motor 80 to 58.33 hertz, which is done by slowing motor 80 until a predetermined negative error is output by comparator 70 on line 73 and sensed by control logic 72.

Lastly, if phase comparator circuit 70 senses the vertical sync VSYNC pulse to be 13.88 milliseconds,

corresponding to 72 hertz which is typical of computer graphics information and data, while a wheel sync period is about 16.66ms, then a positive error is generated by comparator 70. The speed control logic 72 responds to the positive error by instructing motor winding control 76 to rotate motor 80 at 60 hertz, which is done by adjusting the speed of motor 80 and wheel 50 until a predetermined positive error is provided on line 73 by comparator 70 and sensed by logic circuit 72. In summary, control circuit 12 controls the speed of motor 80 as a function of a sensed error signal from comparator 70 to ensure color wheel 50 rotates at a wheel rate being a ratio of integers when compared to the frame rate of data provided on line 20. This is done by establishing a zero error, or a predetermined positive or negative error from comparator 70.

By way of illustration, and first referring to Figure 3, in the case when the incoming frame of data is received at 50 hertz, and the period of VSYNC is 20ms, the color wheel is shown to be rotated at 58.33 hertz, which is a 7/6 ratio. As can be seen from Figure 3a, for the first frame of data to be displayed, shown at 82, seven colored segments of wheel 50 will be illuminated. For the first frame of data 82 to be displayed, three blue segments will be illuminated, while two green segments will be illuminated, and two red segments will be illuminated as well. It is to be noted that the transition time between the first frame of data and a second frame of data, shown at 84, occurs when a spoke (interface) between a blue and green segment, shown at 86, is being illuminated by lamp 56. When the second frame of data, shown at 88, is to be displayed, three green segments will be illuminated wherein two red segments will be illuminated, and two blue segments will also be illuminated. The transition in time between the display of the second frame of data and the third frame of data 90, shown at 96, occurs when the spoke or interface 92 between the green and red segment is being illuminated.

It can be seen that the transition in time between the frames of source data will always correspond to when an interface or spoke between a pair of colored segments of wheel 50 is being illuminated. With color wheel 50 being partitioned into six segments, having three pairs of different colored segments, namely, red, green and blue, the ratio of integers can be maintained. When the incoming data frame rate is less than 60 hertz display rate, a segment of one color of the wheel will be displayed one more time than the other two colored segments. This is a blue colored segment for frame 1, a green segment for frame 2, and a red segment for frame 3, with this pattern repeating for subsequent displayed frames of data. For successive frames of data, a segment of one color is illuminated one more time than the others. Due to the integration of color by the human eye, minimal or no artifacts are generated that are observable by the human eye, depending on the type of data source. In the example shown in Figure 2, a blue colored segment will be illuminated three times for data

frames one and four, a green segment will be illuminated three times for frames two and five, and a red segment will be illuminated three times for frames three and six.

The start of the read cycle of the memory banks 26 and 28, identified as DRAM A and DRAM B, is shown to correspond with the transitions between the frames of data, i.e., that shown at 84 and 96 etc.

Referring now to Figure 4, there is illustrated the spoke-synchronous frame rate conversion process according to the present invention when the source frame rate is greater than 60 hertz, i.e. 72 hertz, corresponding to computer graphics information. In this illustration, the color wheel is rotated at 60 hertz, thereby establishing a 5-to-6 ratio of the color wheel rate in reference to the source frame rate. The first frame of data to be displayed is shown at 100, the second frame of data is shown at 102, with the transition time being shown at 104. The transition 104 in time is seen to correspond to the spoke or transition between a green and red colored segment being illuminated by source 56. As can be seen in Figure 4a, for the first frame of data to be displayed, the first five sequentially presented color segments are utilized, namely, blue, green, red, blue and green. When displaying the second frame of data, a red segment from the first rotation of the color wheel is utilized, whereby the next four illuminated color segments of the second rotation of the color wheel are illuminated. It can be seen that the transition in time between each of the displayed data frames always corresponds to the spoke (interface) between a pair of colored segments. Moreover, the video frame rate displayed is still 60 hertz, corresponding to the color wheel rate. For each frame of data to be displayed, a segment of one color will be illuminated only once, wherein segments of the other two colors will each be illuminated twice.

For example, when the first data frame is to be displayed, two blue colored segments are illuminated, two green colored segments are illuminated, however, a red colored segment is illuminated only once. The next time a red colored segment is illuminated is when the second frame of data is to be displayed. As with the example of Figure 3, the color of a segment which is illuminated more times or less times than the other two colored segments shifts from data frame to data frame. Again, due to the integration of color by the human eye, minimal or no visible artifacts will be perceived by the human eye. The pattern of where the transition in time between the data frames occurs with respect to the color wheel repeats over 5 rotations of the wheel, as shown.

The present invention achieves technical advantages by synchronizing the source data frame rate to the various spokes of the color wheel. In this way, the transition time from frame to frame always occurs in association with the spokes of the color wheel. No data is discarded or filtered before video data is displayed. The mismatch of bandwidths between the input source video and the output sequential color display is accommodated, whereby no mixing of frame-to-frame bit

planes during a color segment is required. The video frame rate is still maintained at about 60 hertz, and minimal or no visual artifacts are perceived by the human eye.

Though the invention has been described with respect to a specific preferred embodiment, many variations and modifications will become apparent to those skilled in the art upon reading the present application.

Claims

1. A display system, comprising:

a spatial light modulator;
 a light source providing light;
 a color wheel rotating at a wheel rate and coloring said light, said colored light illuminating said spatial light modulator;
 a logic circuit having an input receiving frames of data at a frame rate and communicating said data to said spatial light modulator; and
 a control circuit detecting said frame rate, and adjusting said color wheel rate so that said wheel rate compared to said frame rate is a ratio of integers, said integer ratio being other than one-to-one.

2. The display system as specified in Claim 1, wherein said color wheel has a plurality of colored segments, each said colored segment, but one or more, is illuminated for each said frame of data when said frame rate is less than said wheel rate.

3. The display system as specified in Claim 1 or Claim 2, wherein said color wheel has a plurality of colored segments, at least one said colored segment being illuminated one more time than said other colored segments when said frame rate is greater than said wheel rate.

4. The display system as specified in any preceding claim, wherein said color wheel has multiple colored segments, each said colored segment except one or more being illuminated equally in time for each said frame of data by said light source when said frame rate is less than said wheel rate.

5. The display system as specified in any preceding claim, wherein said color wheel has multiple colored segments, each said colored segment except one or more being illuminated equally in time for each said frame of data by said light source when said frame rate is greater than said wheel rate.

6. The display system as specified in any preceding claim, wherein said color wheel has n colored segments, no more than n-1 said segments being illuminated for each said data frame when said frame

rate is less than said wheel rate.

7. The display system as specified in any preceding claim, wherein said color wheel has n colored segments, wherein at least n-1 said segments are illuminated each said data frame when said frame rate is greater than said wheel rate.

8. The display system as specified in any preceding claim, wherein the ratio of said frame rate to said wheel rate is 7-to-6 when said frame rate is less than said wheel rate.

9. The display system as specified in any preceding claim, wherein the ratio of said frame rate to said wheel rate is 5-to-6 when said frame rate is greater than said wheel rate.

10. The display system as specified in any preceding claim, wherein said control circuit is responsive to a vertical sync rate signal corresponding to said frame rate, and provides a wheel motor sync output at a rate being a function of said vertical sync rate signal of said frames of data, said wheel motor sync output being unequal to said vertical sync rate signal when said vertical sync rate deviates from a predetermined rate, said wheel rate being a function of said sync wheel motor output.

11. The display system as specified in Claim 10 wherein said wheel motor sync output rate is identical to said vertical sync rate when said frame rate is identical to said predetermined rate.

12. A method of operating a sequential display system, comprising:

operating a color wheel having a plurality of colored segments to color source light illuminating a spatial light modulator, said color wheel rotating at a wheel rate;
 modulating said colored light with said spatial light modulator according to frames of data received at a frame rate; and
 controlling said wheel rate as a function of said frame rate to be a ratio of integers with respect to said frame rate, wherein said ratio of integers is other than 1-to-1.

13. The method as specified in Claim 12, further comprising providing a color wheel having at least six colored segments.

14. The method as specified in Claim 12 or Claim 13, wherein said step of operating said color wheel comprises selecting a transition time between said frames of data when a transition between said wheel colored segments is at a predetermined position relative to said source light.

15. The method as specified in Claim 14 wherein said selecting step comprises selecting said predetermined position such that said source light illuminates said transition between said colored segments.

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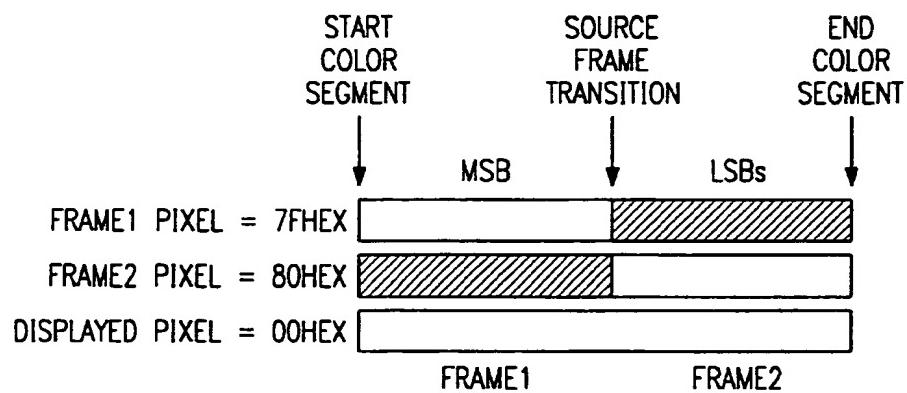
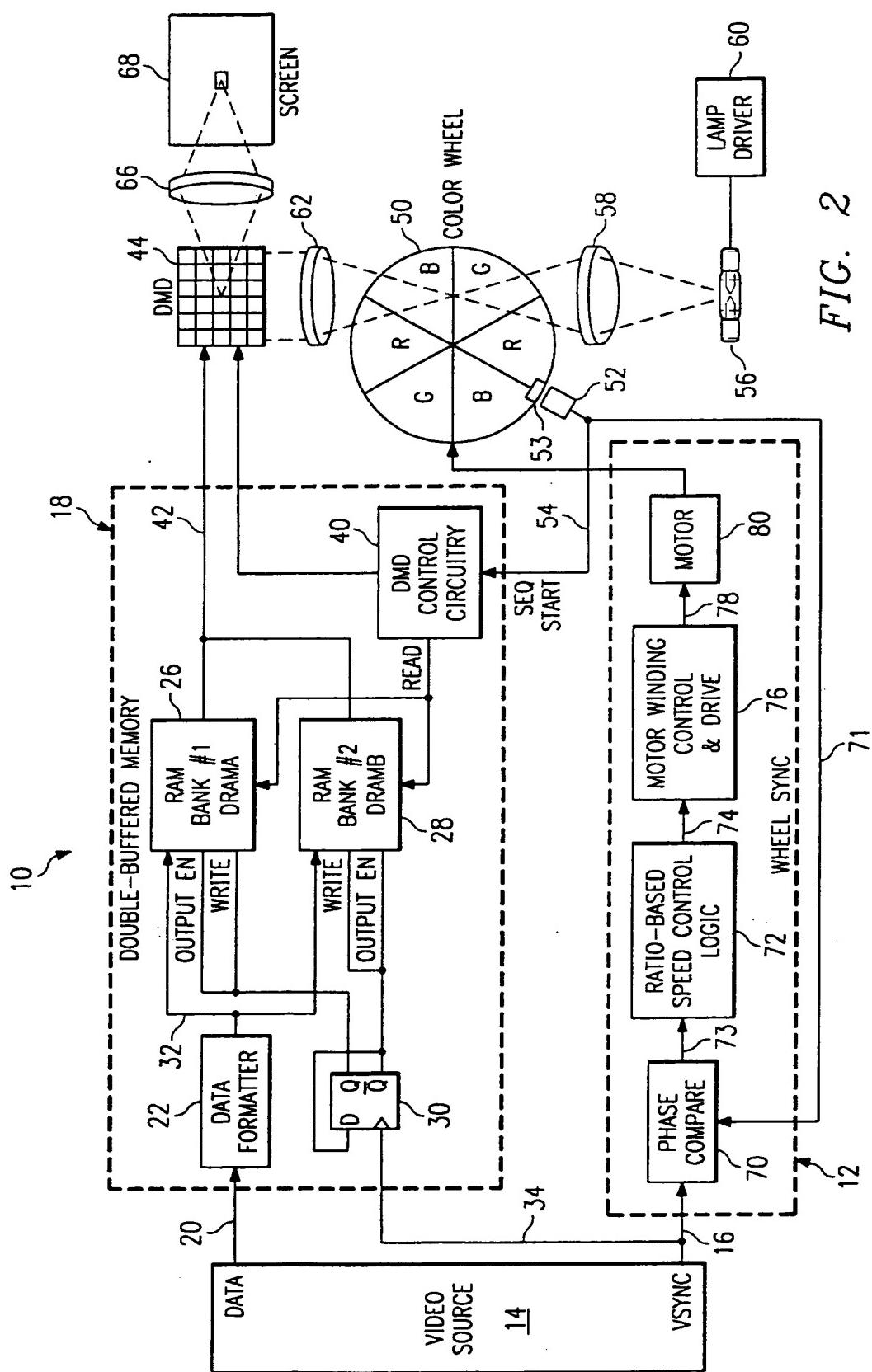


FIG. 1



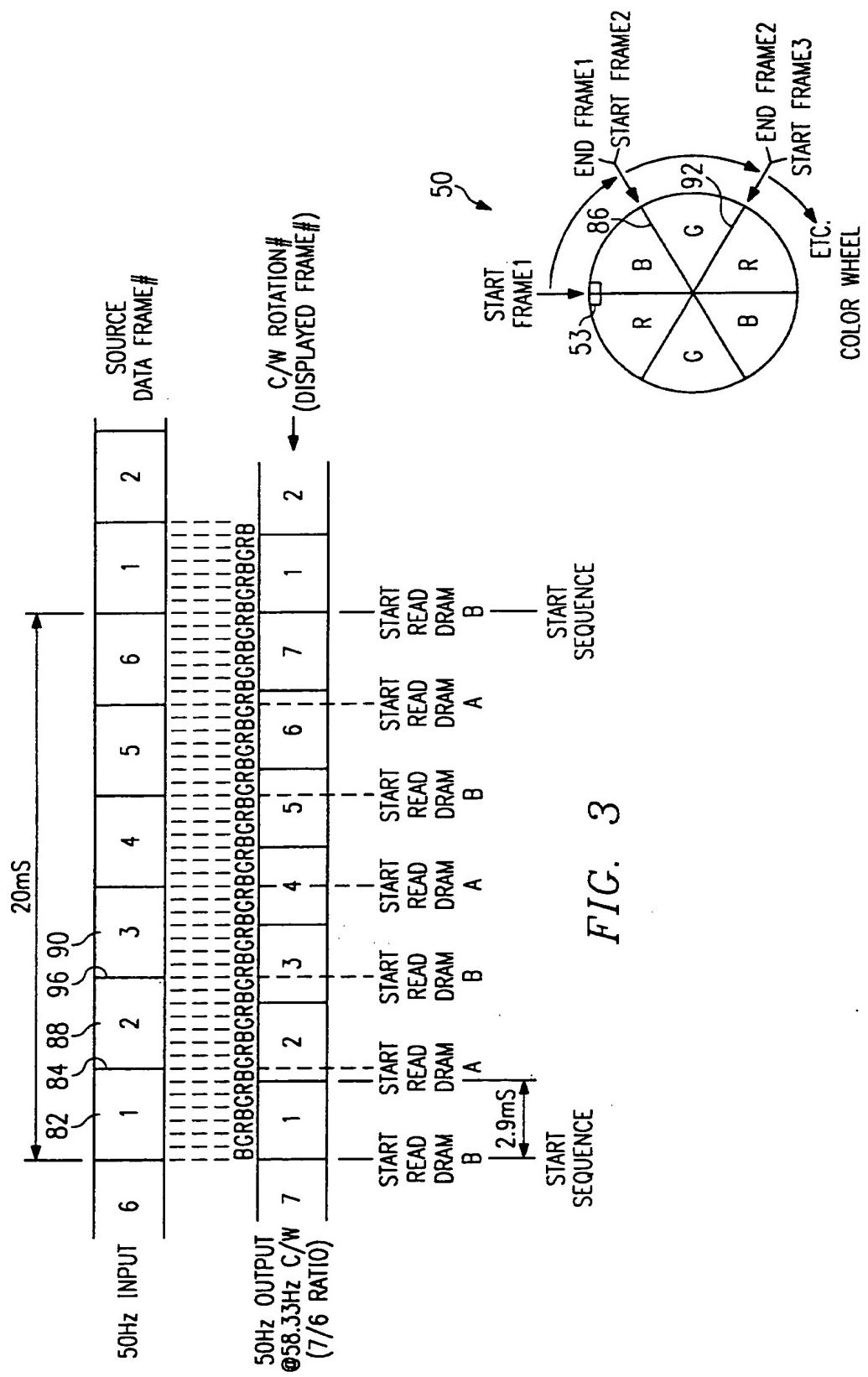


FIG. 3

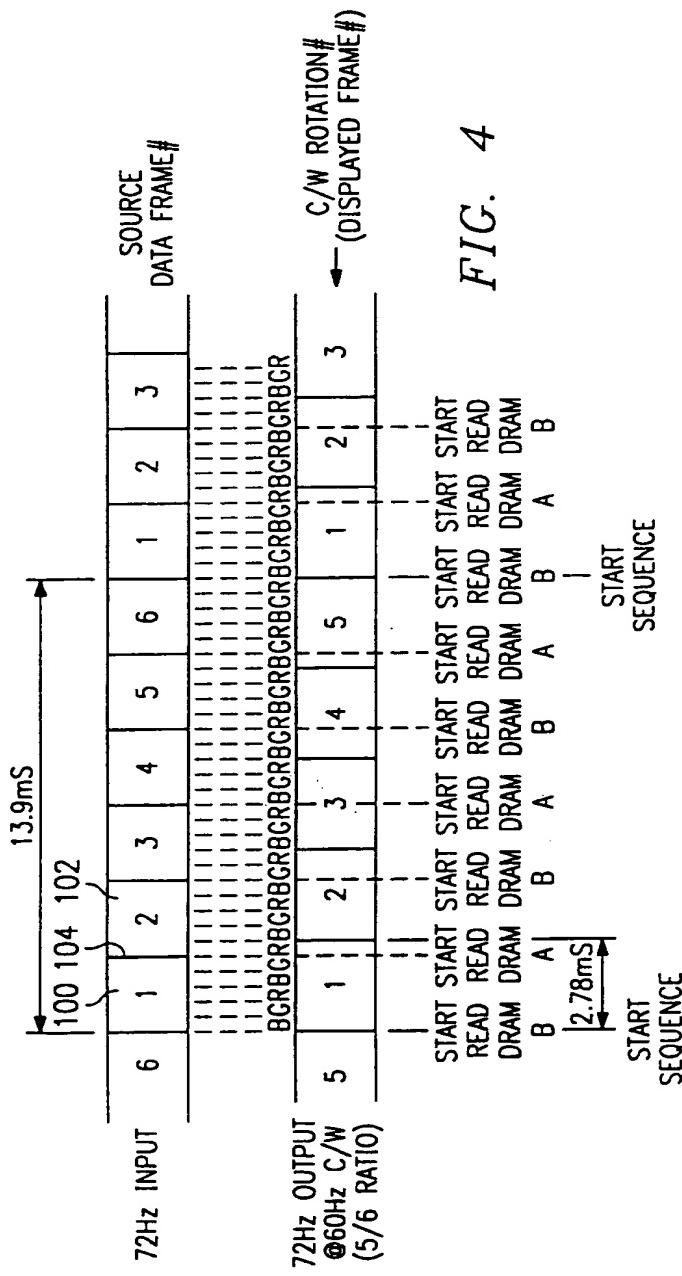


FIG. 4

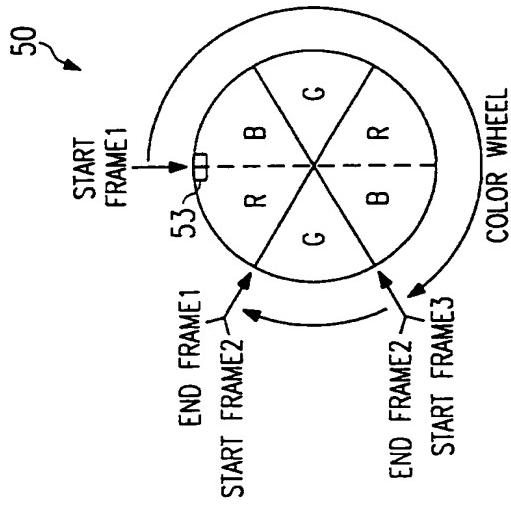


FIG. 4A



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Application Number
EP 96 10 9186

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.)
A	PATENT ABSTRACTS OF JAPAN vol. 95, no. 02, 31 March 1995 & JP-A-86 308527 (MIYOTA KK), 4 November 1994, * abstract *---	1-15	H04N9/31
A	IEEE SPECTRUM, vol. 30, no. 11, 1 November 1993, pages 27-31, XP000418839 YOUNSE J M: "MIRRORS ON A CHIP" * page 28, column 2, line 11 - page 30, column 1, line 11 *---	1-15	
A	GB-A-2 132 387 (WELCH ALLYN INC) 4 July 1984 * page 1, line 122 - page 3, line 103 *---	1-15	
A,P	EP-A-0 662 773 (TEXAS INSTRUMENTS INC) 12 July 1995 * column 4, line 33 - column 11, line 1 *---	1-15	
D	& US-A-5 448 314 ---		
A,D	US-A-5 061 049 (HORNBECK LARRY J) 29 October 1991 * column 9, line 15 - column 28, line 40 *---	1-15	H04N G02B
A	EP-A-0 635 986 (TEXAS INSTRUMENTS INC) 25 January 1995 * column 6, line 44 - column 17, line 36 *---	1-15	
D	& US-A-5 079 544 ---		
D,A	US-A-5 105 369 (NELSON WILLIAM E) 14 April 1992 * column 4, line 55 - column 11, line 3 *---	1-15	
		-/-	
The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
BERLIN	27 August 1996	Materne, A	
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			



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Application Number
EP 96 10 9186

DOCUMENTS CONSIDERED TO BE RELEVANT			CLASSIFICATION OF THE APPLICATION (Int.Cl.)
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	
A,P	US-A-5 523 803 (URBANUS PAUL M ET AL) 4 June 1996 * column 3, line 49 - column 9, line 45 *	1-15	
D	& US-A-5 278 652 ---		
A,P	EP-A-0 663 770 (TEXAS INSTRUMENTS INC) 19 July 1995 * column 4, line 10 - column 12, line 42 *	1-15	
D	& US-A-5 452 024 ---		
D,A	US-A-5 101 236 (NELSON WILLIAM E ET AL) 31 March 1992 * column 5, line 48 - column 11, line 63 *	1-15	
			TECHNICAL FIELDS SEARCHED (Int.Cl.)
The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
BERLIN	27 August 1996	Materne, A	
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<small>EPO FORM 1500/02 (P0201)</small>			